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PATENT
RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2633
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on August 27, 2004.


C. Healion

Confirmation No. 4983

Appl No. : 09/782,687
Applicant : Jafar Savoj
Filed : February 12, 2001
Title : LINEAR HALF RATE PHASE DETECTOR AND CLOCK AND DATA RECOVERY CIRCUIT

TC/A.U. : 2633
Examiner : Nathan M. Curs

Docket No. : 50991/SDB/B600
Customer No. : 23363

AMENDMENT

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
August 27, 2004

Commissioner:

In response to the Office action of June 1, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.